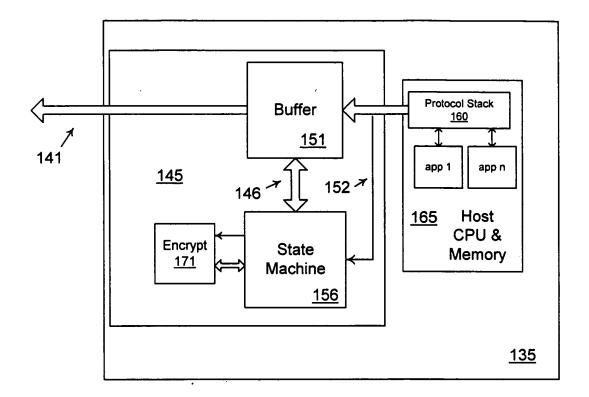


Fig. 1A



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Fig. 1B

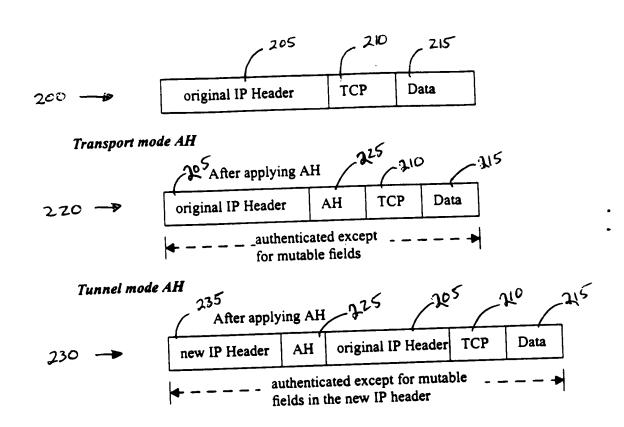


FIG. 2A

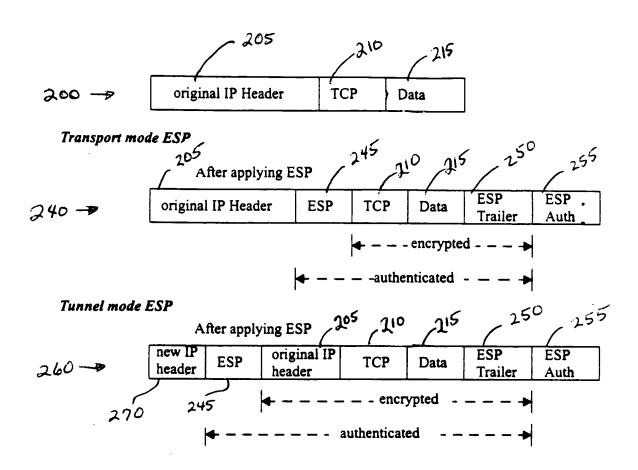


FIG. 2B

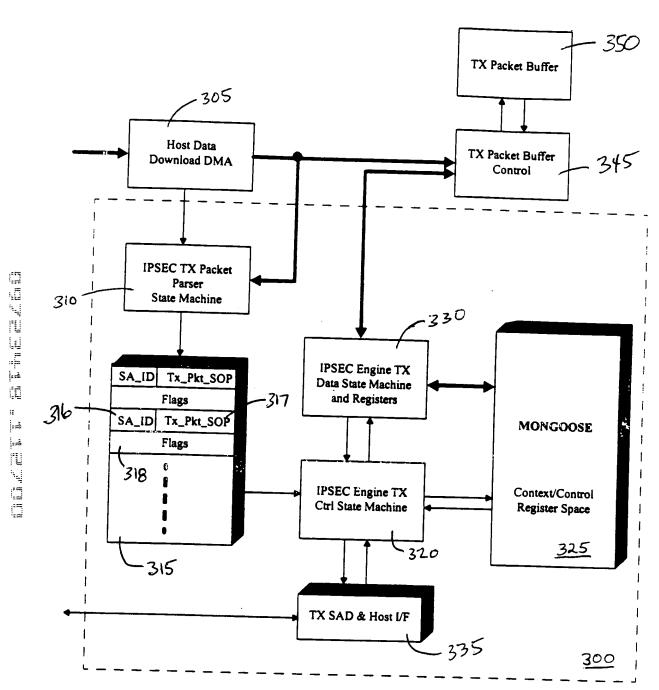


Figure 3 Hardware architecture for outbound traffics

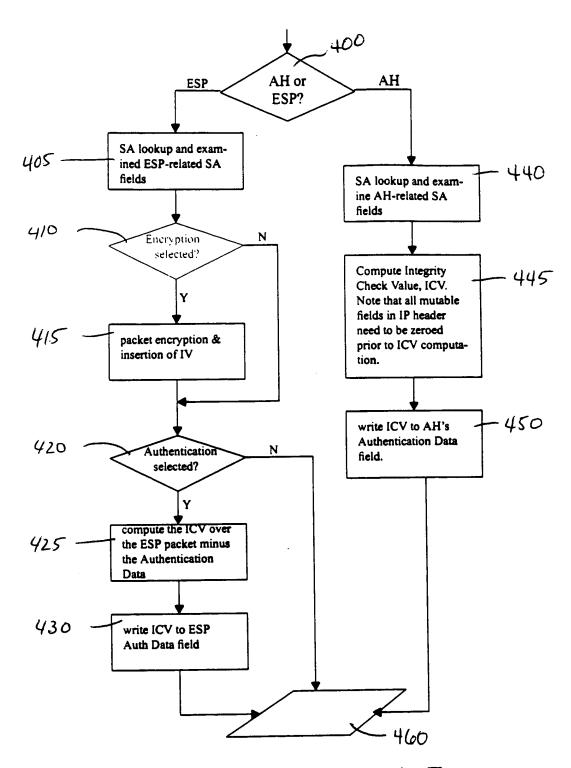


Figure 4. Outbound IP Traffic Processing Flow

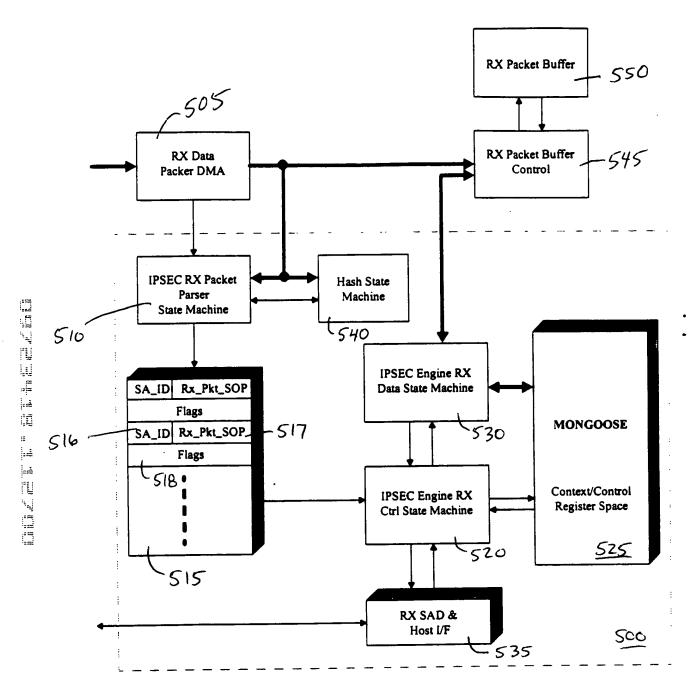


Figure 5 Hardware architecture for inbound traffics

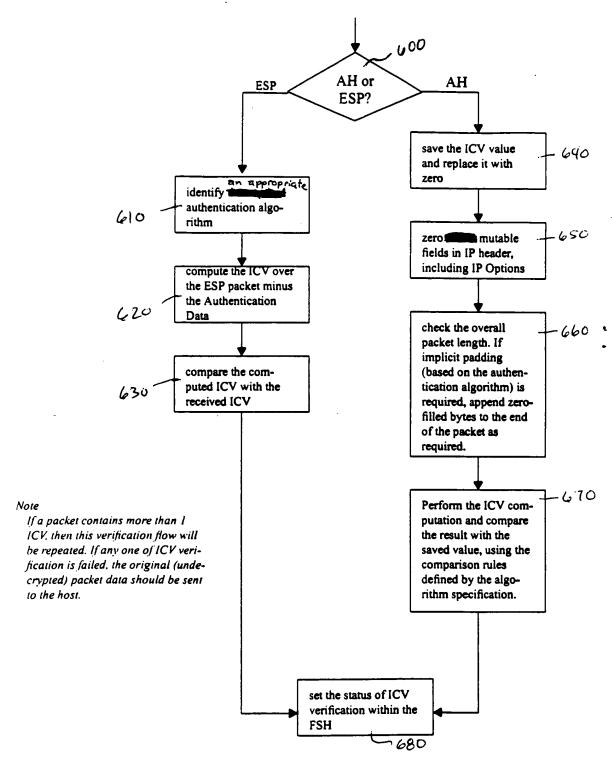


Figure 6. ICV verification for inbound traffic